

The
United
States
of
America



**The Director of the United States
Patent and Trademark Office**

Has received an application for a patent for a new and useful invention. The title and description of the invention are enclosed. The requirements of law have been complied with, and it has been determined that a patent on the invention shall be granted under the law.

Therefore, this

United States Patent

Grants to the person(s) having title to this patent the right to exclude others from making, using, offering for sale, or selling the invention throughout the United States of America or importing the invention into the United States of America, and if the invention is a process, of the right to exclude others from using, offering for sale or selling throughout the United States of America, or importing into the United States of America, products made by that process, for the term set forth in 35 U.S.C. 154(a)(2) or (c)(1), subject to the payment of maintenance fees as provided by 35 U.S.C. 41(b). See the Maintenance Fee Notice on the inside of the cover.

Michelle K. Lee

Director of the United States Patent and Trademark Office



US009433103B2

(12) **United States Patent**
Chung et al.

(10) **Patent No.:** **US 9,433,103 B2**
(45) **Date of Patent:** ***Aug. 30, 2016**

(54) **METHOD OF MANUFACTURING A MULTICHIP PACKAGE STRUCTURE**

2201/2054 (2013.01); Y10T 29/49146 (2015.01); Y10T 29/49147 (2015.01)

(71) Applicant: **PARAGON SEMICONDUCTOR LIGHTING TECHNOLOGY CO., LTD.**, New Taipei (TW)

(58) **Field of Classification Search**
CPC H01L 25/0753; H05K 3/284; H05K 1/0203; H05K 2201/2054; H05K 2201/10106; Y10T 29/49146–29/49147
USPC 257/88, 91; 313/46; 362/235; 315/294
See application file for complete search history.

(72) Inventors: **Chia-Tin Chung**, Miaoli County (TW); **Chao-Chin Wu**, Taipei (TW); **Fang-Kuei Wu**, Taoyuan County (TW)

(56) **References Cited**

(73) Assignee: **PARAGON SEMICONDUCTOR LIGHTING TECHNOLOGY CO., LTD.**, New Taipei (TW)

U.S. PATENT DOCUMENTS

(*) Notice: Subject to any disclaimer, the term of this patent is extended or adjusted under 35 U.S.C. 154(b) by 0 days.

This patent is subject to a terminal disclaimer.

2011/0241035 A1 * 10/2011 Chung H01L 25/0753 257/88
2012/0007122 A1 * 1/2012 Choi H01L 33/483 257/98

* cited by examiner

Primary Examiner — Kenneth Parker

Assistant Examiner — Bo Fan

(74) *Attorney, Agent, or Firm* — Li & Cai Intellectual Property (USA) Office

(21) Appl. No.: **14/803,185**

(22) Filed: **Jul. 20, 2015**

(65) **Prior Publication Data**

US 2015/0327373 A1 Nov. 12, 2015

Related U.S. Application Data

(62) Division of application No. 13/425,692, filed on Mar. 21, 2012, now Pat. No. 9,125,328.

(51) **Int. Cl.**
H05K 3/28 (2006.01)
H01L 33/54 (2010.01)
H01L 33/60 (2010.01)

(52) **U.S. Cl.**
CPC **H05K 3/285** (2013.01); **H01L 33/54** (2013.01); **H01L 33/60** (2013.01); **H05K 3/284** (2013.01); **H01L 2224/05553** (2013.01); **H01L 2224/48091** (2013.01); **H01L 2924/181** (2013.01); **H05K 2201/10106** (2013.01); **H05K**

(57) **ABSTRACT**

A method of manufacturing a multichip package structure includes: providing a substrate body; placing a plurality of light-emitting chips on the substrate body, where the light-emitting chips are electrically connected to the substrate body and surrounded by the semidrying surrounding light-reflecting frame; naturally drying an outer layer of the surrounding liquid colloid at a predetermined room temperature to form a semidrying surrounding light-reflecting frame, where the semidrying surrounding light-reflecting frame has a non-drying surrounding colloid body disposed on the substrate body and a dried colloid outer layer totally covering the non-drying surrounding colloid body; and then forming a package colloid body on the substrate body to cover the light-emitting chips, where the semidrying surrounding light-reflecting frame contacts and surrounds the package colloid body.

5 Claims, 11 Drawing Sheets

